

Fig. 1

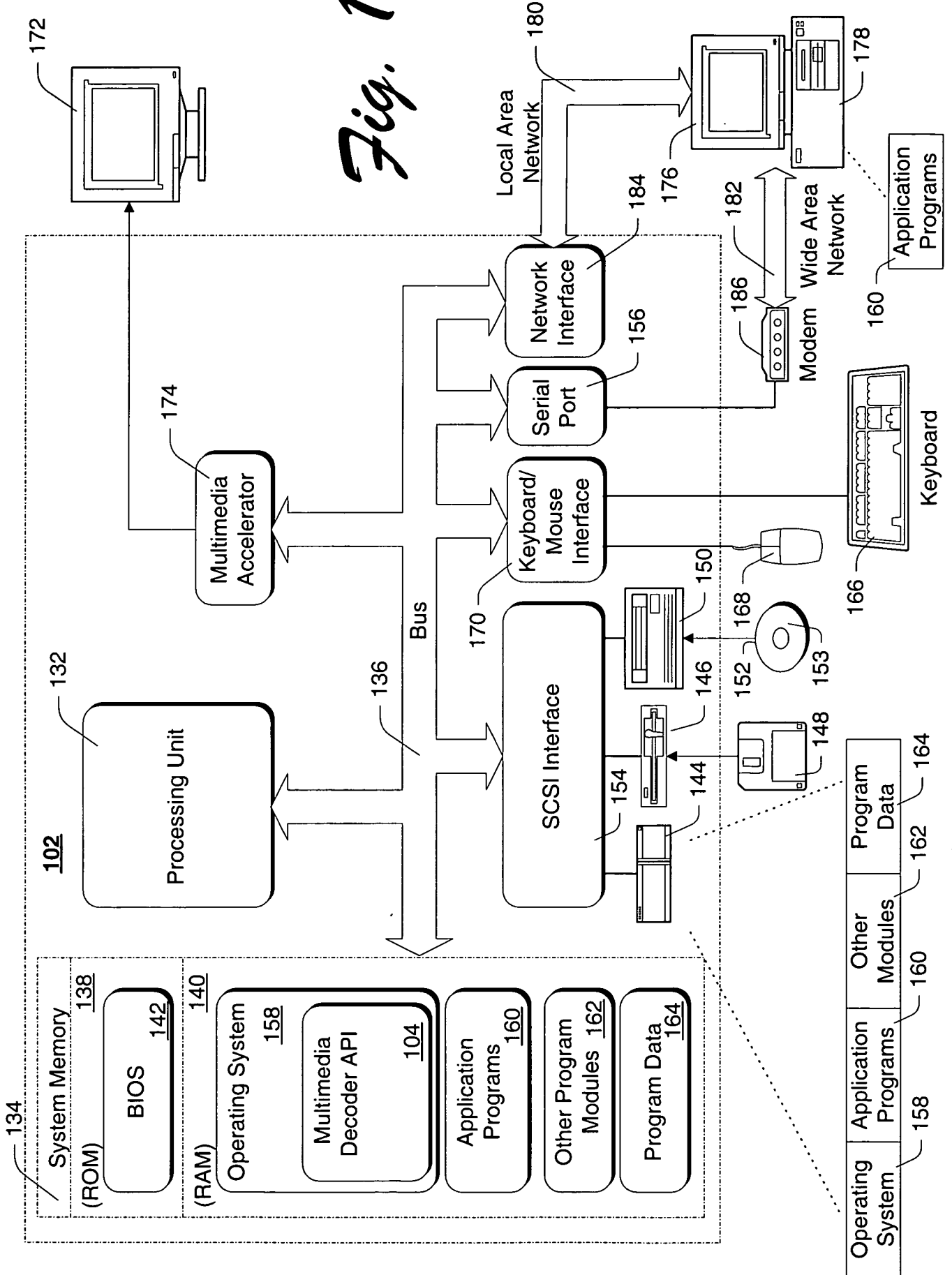


Fig. 2

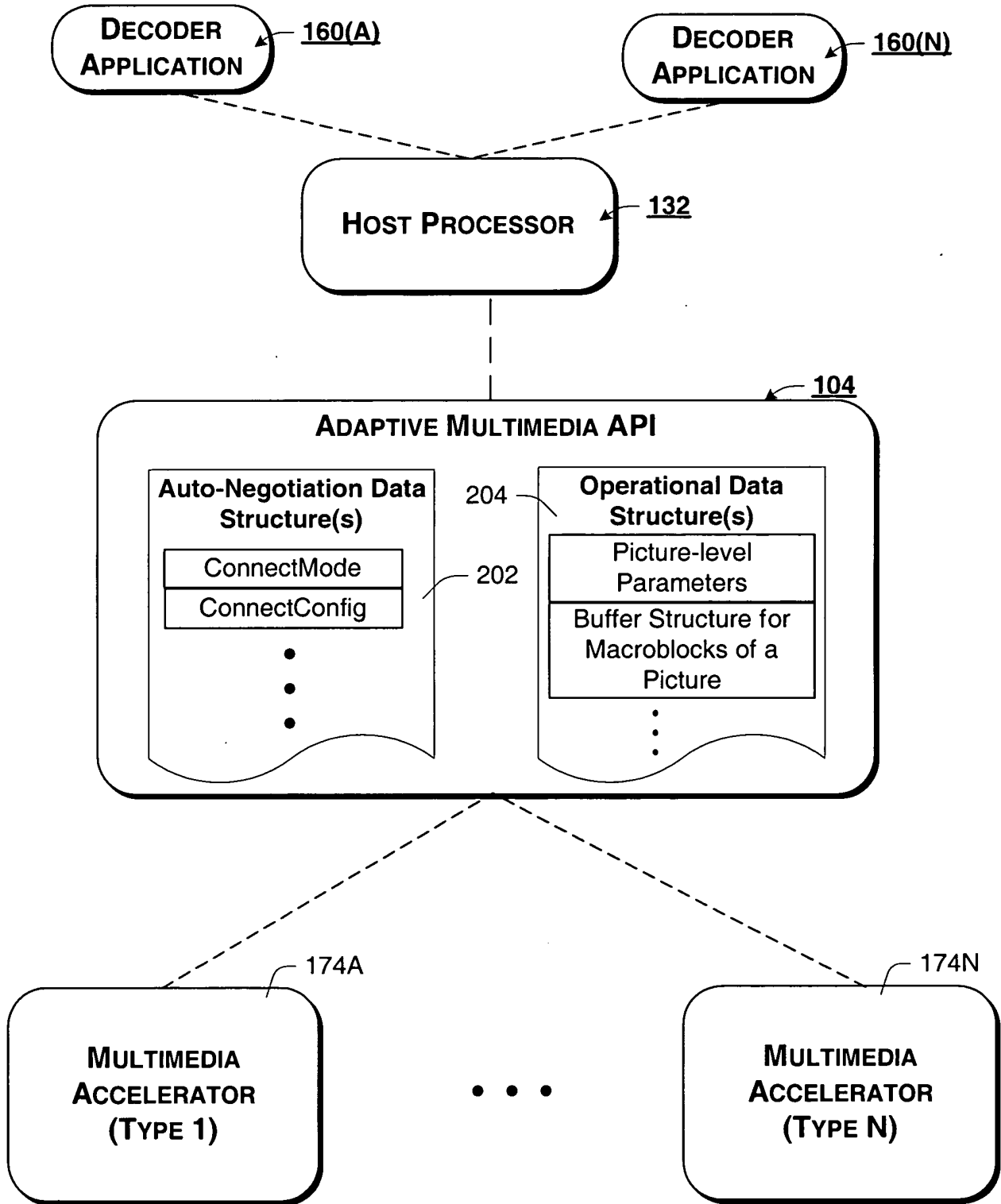


FIG. 2 is a block diagram of a multimedia system architecture.

Fig. 3

300

302		304	306
MB Address	RD Pointer	Control Operation	
...			

Fig. 4

400

402	Residual Difference Data
	Data for MB 3
	Data for MB 1
	Data for MB 2
	...

Fig. 5

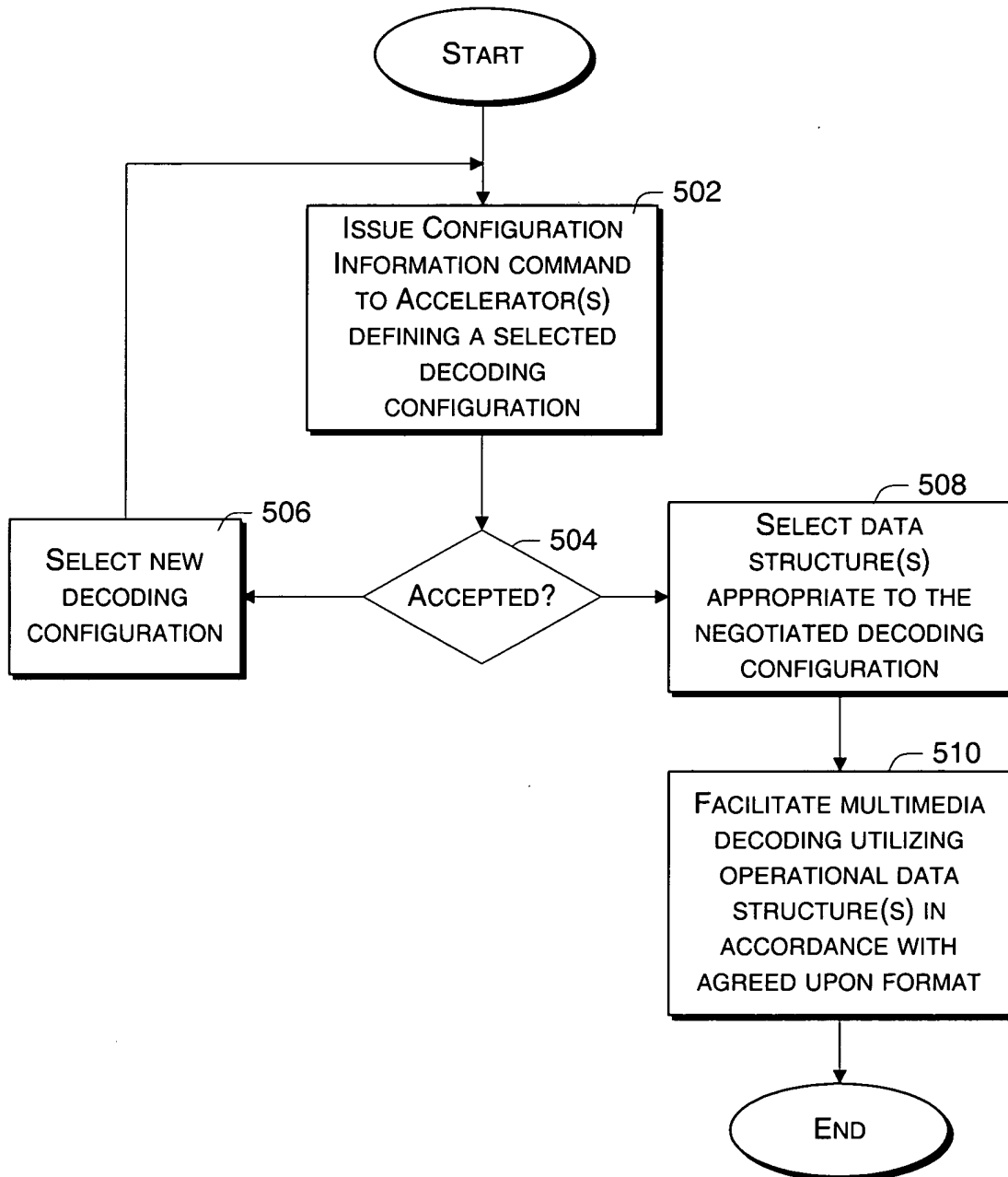
500

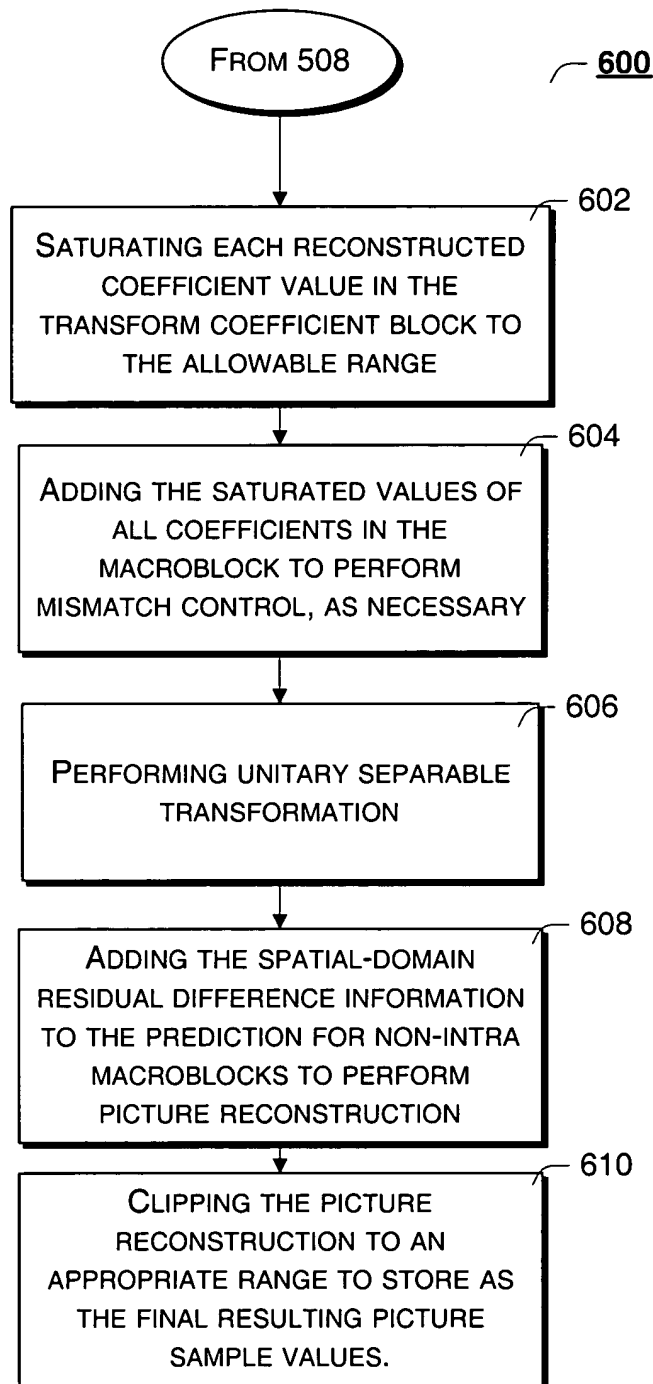
Fig. 6

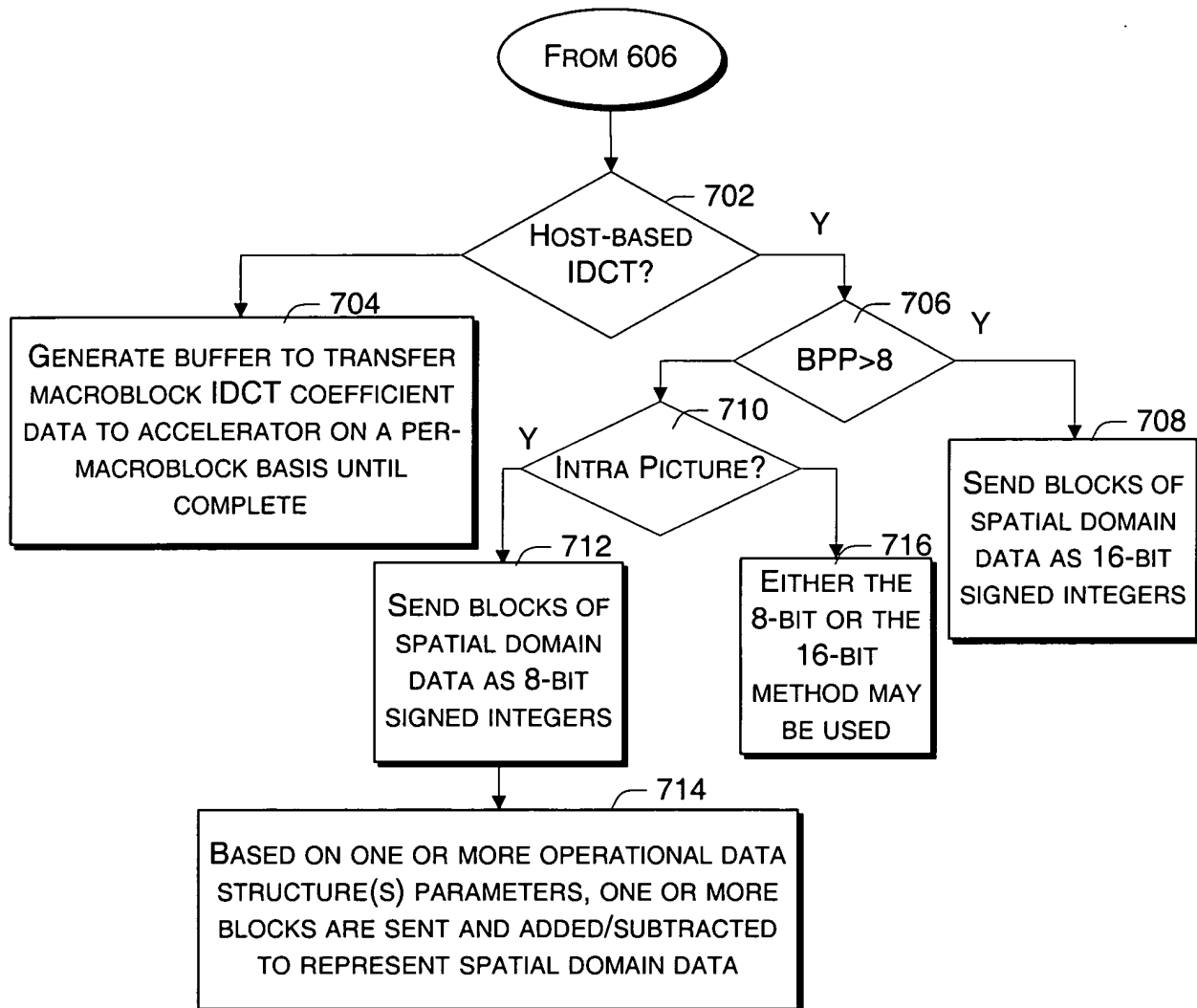
Fig. 7

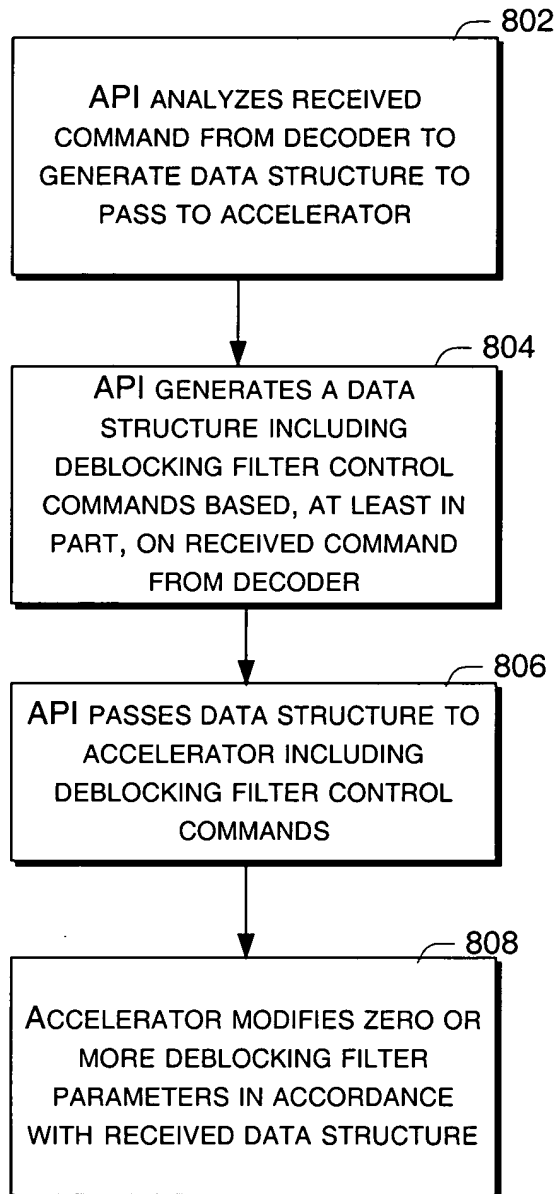
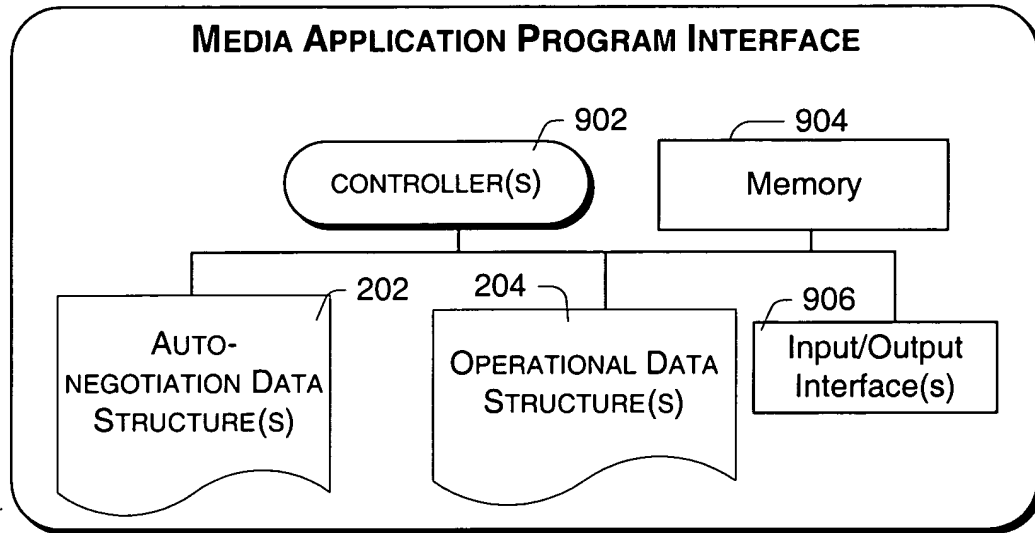
Fig. 8

FIG. 8 is a flowchart of the process.

*Fig. 9*900 →*Fig. 10*→ 1000